

Docket No. 211200US2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Toshitada SAITO

FILING DATE: Herewith

FOR: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT INCLUDING PROCESSOR CIRCUIT  
AND ITS PERIPHERAL CIRCUITS

**LIST OF INVENTOR'S NAME AND ADDRESS**

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Listed below is the name and address of the inventor for the above-identified patent application.

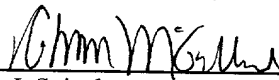
Toshitada SAITO

YOKOHAMA-SHI, JAPAN

A declaration containing all the necessary information will be submitted at a later date.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



Marvin J. Spivak

Registration No. 24,913

Obi, Spivak, McClelland,  
Maier & Neustadt, P.C.

211200US2



**22850**

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 11/98)

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type:: REGULAR  
Subject Matter:: UTILITY  
CD-ROM or CD-R?: NONE  
Title:: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED  
CIRCUIT INCLUDING PROCESSOR CIRCUIT AND  
ITS PERIPHERAL CIRCUITS  
Attorney Docket Number:: 211200US2  
Total Drawing Sheets:: 11

INVENTOR INFORMATION

Inventor Authority Type:: INVENTOR  
Status:: FULL CAPACITY  
Given Name:: Toshitada  
Family Name:: SAITO  
City of Residence:: YOKOHAMA-SHI  
State or Prov. of Residence:: KANAGAWA-KEN  
Country of Residence:: JAPAN  
Street:: 689-1-315, TERAYAMA-CHO, MIDORI-KU  
City:: YOKOHAMA-SHI  
State or Province:: KANAGAWA-KEN  
Country:: JAPAN

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

FOREIGN PRIORITY INFORMATION

Country:: JAPAN  
Application Number:: 2000-208601  
Filing Date:: 07/10/00

ASSIGNMENT INFORMATION

Assignee Name:: KABUSHIKI KAISHA TOSHIBA  
Street:: 72, HORIKAWA-CHO,  
SAIWAI-KU  
City:: KAWASAKI-SHI  
State or Province:: KANAGAWA-KEN  
Country:: JAPAN